

FORM PTO-892		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 09/105,528	GROUP ART UNIT 2171	ATTACHMENT TO PAPER NO.	22
NOTICE OF REFERENCES CITED				APPLICANT(S) Knowles			
U.S. PATENT DOCUMENTS							
*		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	A						
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	C						
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FOREIGN PATENT DOCUMENTS							
*		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB- CLASS
	L						
	M						
	N						
	O						
	P						
	Q						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	R	Gedye et al "Browsing the chip design database", IEEE 1988, pages 269-274.					
	S						
	T						
	U						
EXAMINER Uyen Le			DATE June 15, 2003				
Form892ccs2106b							
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05(a).)							